



Docket No.: 9323.055.00-US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

In-Kwon Jeong

Application No.: 09/828,029

Customer No.: 30827

Filed: April 6, 2001

Confirmation No.: 6430

For: MULTI-CHANNEL TEMPERATURE
CONTROL SYSTEM FOR
SEMICONDUCTOR PROCESSING
FACILITIES

Art Unit: 3743

Examiner: L. V. Ceric

REQUEST FOR CORRECTION OF INVENTORSHIP UNDER 37 C.F.R 1.48(A)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

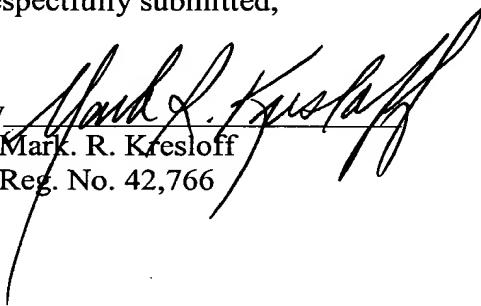
This submission accompanies a Request for Continued Examination filed herewith. The Applicant wishes to correct the inventorship of the above-referenced application by deleting the originally named inventor, In-Kwon Jeong, and adding the correct inventor, Boris Atlas. Accordingly, the Applicant submits the following:

1. A statement from Boris Atlas (Correction of Inventorship under 37 C.F.R. § 1.48(a)), the inventor being added, stating that the error occurred without deceptive intention on his part;
2. a new Power of Attorney;
3. the required processing fee set forth under 37 C.F.R. 1.17(i);
4. a Statement under 37 C.F.R. 3.73(a); and
5. a Consent of Assignee Statement under 37 C.F.R. § 1.48(a)(5).

The Applicant's representatives are in the process of attempting to locate the currently named inventor and have not been able to contact him. The Applicant will submit a Statement under 37 C.F.R. 1.48(a)(2) executed by the currently named inventor when he is located. Otherwise, if the inventor cannot be found, a proper petition detailing the steps taken to find the inventor will be submitted along with the appropriate fees.

Dated: January 4, 2005

Respectfully submitted,

By 
Mark. R. Kresloff
Reg. No. 42,766



Docket No.: 9323.055.00
(PATENT)

Declaration and Power of Attorney

WE (I) the undersigned inventor(s), hereby declare(s) that:

My residence, post office address and citizenship are as stated below next to my name,

We (I) believe that we are (I am) the original, first, and joint (sole) inventor(s) of the subject matter which is claimed and for which a patent is sought on the invention entitled

MULTI-CHANNEL TEMPERATURE CONTROL SYSTEM FOR SEMICONDUCTOR PROCESSING FACILITIES

the specification of which

<input type="checkbox"/>	is attached hereto.		
<input checked="" type="checkbox"/>	was filed on	April 6, 2001	
		as Application No.	09/828,029
		and amended on	
<input type="checkbox"/>	was filed as PCT international application		
Number			
	on		
and was amended under PCT Article 19			
on		(if applicable).	

We (I) hereby state that we (I) have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

We (I) acknowledge the duty to disclose information known to be material to the patentability of this application as defined in Section 1.56 of Title 37 Code of Federal Regulations.

We (I) hereby claim foreign priority benefits under 35 U.S.C. §119(a)-(d) or §365(b) of any foreign application(s) for patent or inventor's certificate, or §365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed. Prior Foreign Application(s)

Application No.	Country	Day/Month/Year	Priority Claimed			
			<input type="checkbox"/>	Yes	<input type="checkbox"/>	No
			<input type="checkbox"/>	Yes	<input type="checkbox"/>	No
			<input type="checkbox"/>	Yes	<input type="checkbox"/>	No

We (I) hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

(Application Number)

(Filing Date)

We (I) hereby claim the benefit under 35 U.S.C. §120 of any United States application(s), or §365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. §112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR §1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

Application Serial No.		Filing Date	Status (pending, patented, abandoned)

We (I) hereby appoint the practitioners associated with Customer No. 30827, with full powers of substitution and revocation, to prosecute the patent application identified above and filed herewith and to transact all business in the U.S. Patent and Trademark Office connected therewith; and we (I) hereby request that all correspondence regarding this application be sent to Song K. Jung of McKenna Long & Aldridge LLP, Attorneys At Law, 1900 K Street, N.W., Washington, D.C. 20006.

We (I) declare that all statements made herein of our (my) own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon, and may jeopardize the validity of the patent application or any patent issued thereon.

Boris ATLAS

NAME OF FIRST INVENTOR

B. Atlas

Signature of Inventor

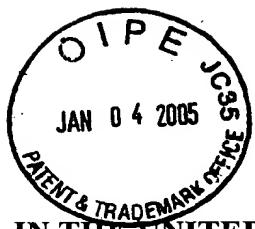
B. 12 - 9 - 04.

Date

Residence: 6247 Royal Oak Court,
San Jose, California 95123

Citizen of: U.S.A.

Post Office Address: Same as Residence



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In-Kwon Jeong

Application No.: 09/828,029

Filed: April 6, 2001

For: **MULTI-CHANNEL TEMPERATURE
CONTROL SYSTEM FOR SEMICONDUCTOR
PROCESSING FACILITIES**

Confirmation No.: 6430

Art Unit: 3753

Examiner: L. V. Cricic

Customer No.: 30827

CORRECTION OF INVENTORSHIP UNDER 37 CFR § 1.48(a)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I, Boris Atlas, state that I have been omitted as the sole inventor for the above-captioned application and hereby request that my name be added as the inventor of record. The error in inventorship occurred without deceptive intent on my part.

Respectfully submitted,

Dated: 12-9-04

By: B. Atlas
Boris Atlas



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In-Kwon Jeong

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Filed: April 6, 2001

Art Unit: 3743

**For: MULTI-CHANNEL TEMPERATURE
CONTROL SYSTEM FOR
SEMICONDUCTOR PROCESSING
FACILITIES**

Examiner: L. V. Cirić

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

Dear Sir:

The assignee, Oriol, Inc., is in bankruptcy. I am the Chapter 7 Trustee and, as such, I am authorized to act on behalf of the assignee regarding the above-identified patent application.

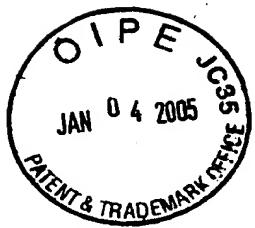
Dated:

1/4/05

Respectfully submitted,

By

Carol W. Wu
Carol W. Wu, Esq.
Chapter 7 Trustee



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Examiner: L. V. Ceric

CONSENT OF ASSIGNEE UNDER 37 C.F.R. 1.48 (A)(5)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

I hereby consent to the correction of inventorship of the above-referenced application under 37 C.F.R. § 1.48(a) such that In-Kwon Jeong is no longer the named inventor and Boris Atlas is the named inventor.

Dated: 1/4/05

Respectfully submitted,

By Carol W. Wu, Esq.
Carol W. Wu, Esq.
Chapter 7 Trustee